



ON THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Balasubramanian, et al.

Art Unit: 2824

Serial No.: 10/768,131

Examiner: Eric J. Wendler

Filed: 02/02/04

Docket: TI-36687

For: Tracking Circuit Enabling Quick/Accurate Retrieval of Data Stored in a Memory Array

**LETTER TO THE OFFICIAL DRAFTSPERSON**

**MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)**

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz  
Karen Vertz

12-1-05

Date

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are **SIX (6)** sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Respectfully submitted,

Alan K. Stewart  
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